

IN THE SPECIFICATION

Please replace the paragraph 0028 as follows:

[0028] A microcontroller 34 is typically provided to control the DSP 30 as well as the interface controller 36 to enable data to be passed to and from the host interface (not shown) in known manner. A data memory 38 may be provided, if desired, to buffer data being written to and read from the disk 12.

Please replace paragraph 0032 as follows:

[0032] The mode of the circuit is controlled by two MOS transistors 60 and 62, which have a selection signal applied to their respective gates on lines 64 and 66. When driving the piezo motor in voltage mode, the 1X output 52 of the OTA 46 is disabled and the nX output 54 is used to create a voltage feedback loop through MOS transistor 62. In charge mode, MOS transistor 60 conducts, coupling the nX output 54 to analog ground, AGND 74.